

MARKING

main processor element SiP, for example L1

supplementary processor element SiP, for example R1

MC p 04 1 1 1 001 01— Q 208 I

Core/system marking

Architecture type

MC - multicellular

Product type

p – processor
c – core
s – system in package (SiP)

Amount of core cells

04, 08, 16 ... – four, eight, sixteen cells etc.

Core type

0 – fixed point
1 – floating point
2 – double precision floating point

Series

1, P – high Performance and simultaneous reduction of power consumption
C – ultra-low power Consumption and high performance
L – Liveness property, fault tolerance
R – dynamic Reconfiguration

Core version or SiP

1
2
...

Supplementary processor/system marking

Assignment

001 – general industrial application
002 – space and critical applications
...

Implementation

01 – processor's system and program integration number
...

Package type

Q – QFP
LQ – LQFP
TQ – TQFP
PQ – PQFP
CQ – CQFP
B – BGA
CB – CBGA
PB – PBGA
LB – LBGA
MQ – MQFP

Amount of outputs

Temperature range

C – 0° - +70°C
I – -40° - +85°C
A – -40° - +125°C
M – -60° - +125°C
S – -60° - +150°C