MULTICLET is a company dealing with creation and production of highly performant fault tolerant and low power consuming processor cores and processors based on multicellular architecture, as well as devices on their basis.

MULTICLET is a technical term that defines processor core, processor based on multicellular architecture, and device with such processor. Nikolay Streltsov is the author of works on development of multicellular architecture, and as a CTO of **MULTICLET** he also developed multicellular IP cores. The term “multiclet” was coined by Dr. Boris Zyryanov (PhD in Engineering Sciences). Late on he worked on development of multicellular processors and devices as a CEO of **MULTICLET**. In 2003 a prototype of multicellular processor, called synputer, gained a prize in nomination “The best product of the Year” at the forum of new products presented at Annual International Signal Processing Conference in Dallas, USA, organized by IEEE.

Multicellular processor is a result of realization of a new patented architecture. Such processors unlike ones, based on traditional Von-Neumann architecture, operate by means of statements consisting of commands. Realization of all operations within each statement without memory involvement provides processor power increase by 4-5 times and reduction of energy consumption by 10 times.

MULTICLET the company is organized on the principle of "fabless company" with a research office in Yekaterinburg.

Engineers of **MULTICLET** develop products for the following market segments :

- space and aircraft equipment,
- on-board equipment,
- general industrial application,
- FPGA-based systems (special-purpose equpment),
- in-car electronics («intellectual» on-board systems for motor cars),
- desktop teraflops computers,
- "Antihacker" trust-processors for bank applications,
- GLONASS/GPS/Galileo receivers,
- audio processors,
- 3D television,
- mobile and video communication

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